



FIG. 2

(1) Group of Load Instructions

- (1.1) `ldb mem, Rn`
- (1.2) `ldbu mem, Rn`
- (1.3) `ldh mem, Rn`
- (1.4) `ldhu mem, Rn`
- (1.5) `ld mem, Rn`
- (1.6) `ldu mem, Rn`

(2) Group of Resize Instructions

- (2.1) `extb Rn`
- (2.2) `exth Rn`
- (2.3) `ext Rn`

(3) Group of Signed/Unsigned Change Instructions

- (3.1) `sgn Rn`
- (3.2) `unsgn Rn`

(4) Group of Immediate Transfer Instructions

- (4.1) `movb imm8, Rn`
- (4.2) `movbu imm8, Rn`
- (4.3) `movh imm16, Rn`
- (4.4) `movhu imm16, Rn`
- (4.5) `mov imm32, Rn`
- (4.6) `movu imm32, Rn`

(5) Group of Arithmetic and Logical Operation Instructions

- (5.1) `add Rm, Rn`
- (5.2) `sub Rm, Rn`
- (5.3) `cmp Rm, Rn`
- (5.4) `and Rm, Rn`
- (5.5) `or Rm, Rn`

(6) Group of Store Instructions

- (6.1) `st Rn, mem`

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